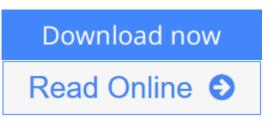


## Field-Programmable Gate Arrays (The Springer International Series in Engineering and Computer Science)

By Stephen D. Brown, Robert J. Francis, Jonathan Rose, Zvonko G. Vranesic



**Field-Programmable Gate Arrays (The Springer International Series in Engineering and Computer Science)** By Stephen D. Brown, Robert J. Francis, Jonathan Rose, Zvonko G. Vranesic

Field-Programmable Gate Arrays (FPGAs) have emerged as an attractive means of implementing logic circuits, providing instant manufacturing turnaround and negligible prototype costs. They hold the promise of replacing much of the VLSI market now held by mask-programmed gate arrays. FPGAs offer an affordable solution for customized VLSI, over a wide variety of applications, and have also opened up new possibilities in designing reconfigurable digital systems. *Field-Programmable Gate Arrays* discusses the most important aspects of FPGAs in a textbook manner. It provides the reader with a focused view of the key issues, using a consistent notation and style of presentation. It provides detailed descriptions of commercially available FPGAs and an in-depth treatment of the FPGA architecture and CAD issues that are the subjects of current research.

The material presented is of interest to a variety of readers, including those who are not familiar with FPGA technology, but wish to be introduced to it, as well as those who already have an understanding of FPGAs, but who are interested in learning about the research directions that are of current interest.

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